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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/645,645
				Filing Date	August 22, 2003
				First Named Inventor	Chandra Mouli
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	M4065.0674/P674
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
qv	AA	6,127,692	10/03/2000	Sugawa et al.	
qv	AB	2002/0024058 A1	02/28/2002	Sugawa et al.	

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ -Kind Code ⁵ (if known)				

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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
qv	CA	V. Chandramouli, et al. – “Design Considerations for High Performance Avalanche Photodiode Multiplication Layers,” IEEE Transactions on Electron Devices, Vol. 41, No. 5, May 1994.		
qv	CB	R. J. McIntyre – “Multiplication Noise in Uniform Avalanche Diodes,” IEEE Transactions on Electron Devices, Vol. ED-13, No. 1, January 1966.		
qv	CC	R. J. McIntyre – “A New Look at Impact Ionization – Part I: A Theory of Gain, Noise, Breakdown Probability, and Frequency Response,” IEEE Transactions on Electron Devices, Vol. 46, No. 8, August 1999.		

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